**B.Tech II Year 4rth Semester**

**Year(2022)**

**Branch ECE**

**Subject : Microprocessor and Microcontroller Final semester**

**Time : 3 Hour M.M : 120**

1. Answer the following questions.
2. How ALE is important for 8085 microprocessor operation.
3. Draw the register organization of 8086 microprocessor.
4. Write the difference between counter type and successive type A/D converter.
5. Write the structure of assembly language in 8051.
6. Define cache memory . Which semiconductor memory is used as cache memory?
7. Answer the following questions.
8. Explain the each bit of flags register for 8085 microprocessor using an example.
9. Describe the function of execution unit and bus interface unit of 8086 microprocessor?
10. Write the short note on programmable interval timer 8254 using proper diagram?
11. Describe the steps for assembling and running an 8051 program using flowchart.
12. Write the notes on enabling and disabling an interrupt also show the bits of interrupt enable register.
13. Part(a) is compulsory and attempt any one part from part(b) or part (c).
14. Explain the register bank of 8085 microprocessor. Also write the difference between program counter and stack pointer of 8085 microprocessor. (6)
15. Draw and explain the pin diagram of 8085 microprocessor. (12)

OR

1. Write a 8085 assembly language program to count continuously in hexadecimal from FFH to 00H in a system with a 0.5µs clock period. Use register C to setup a one millisecond delay between each count and display the numbers at one of the output ports.
2. Part(a) is compulsory and attempt any one part from part(b) or part (c).
3. Write a 8086 assembly language program to convert BCDA data into binary data. (6)
4. Explain briefly about the internal hardware architecture of 8086 microprocessor with a neat (12)

Diagram.

OR

1. Discuss the maximum mode configuration of 8086 with neat diagram, Mention the functions of the various signals.
2. Part(a) is compulsory and attempt any one part from part(b) or part (c).
3. What is DMA ? Explain the DMA based data transfer using DMA controller. (6)
4. Draw and explain the circuit diagram of successive approximation A/D converter for 4 bit binary (12)

data.

OR

1. With a neat functional block diagram , explain the functions of 8259 programmable interrupt controller.
2. Part(a) is compulsory and attempt any one part from part(b) or part (c).
3. Describe the program status word register of 8051 microcontroller. Show how register bank is change by RS1 and RS0 bit. (6)
4. Ten hex numbers are stored in RAM locations 40H onwards. Write a program to find the biggest number in the set. The biggest number should finally saved in 50H. (12)

OR

1. Draw and explain the each bit of TMOS register for Timer 0 and Timer 1. How is the TMOD register modified to make each of the timers operate as counter? Also find the timer’s clock frequency and its period for 8051 microcontroller systems at crystal frequency of 12MHz.
2. Part(a) is compulsory and attempt any one part from part(b) or part (c).
3. Using a proper circuit diagram explain how RS232 is connected with 8051 microcontroller. (6)
4. Draw and explain the circuit diagram for interfacing the keyboard to 8051 microcontroller. (12)

OR

1. Define interrupt in 8051 microcontroller. What are the multiple source of interrupt also explain maskable and non-maskable source of interrupt? Draw the interrupt vector table for the 8051 microcontroller.

www.anysolution.com

www.anysolution.com 